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Date of Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of	)	
	)	Examiner: Not Assigned
DAN TOMESCU, et al.	)	Group Art Unit: 2183
	)	
on DATA PROCESSING SYSTEM	)	
HAVING A CARTESIAN	)	
CONTROLLER	)	
	)	
Serial No.: 10/728,234	)	
	)	
Filed on: December 4, 2003	)	(Our Docket No. 5227-0001-1)

Mail Stop Amendment  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sirs:

In accordance with 37 C.F.R. § 1.56, the references set forth below and listed on the attached form PTO/SB/08A and PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited non U.S. references are enclosed.

<u>U.S. Patent No</u>	<u>Entitled</u>	<u>Dated</u>
4,575,818	Apparatus for in Effect Extending the Width of an Associative Memory by Serial Matching of Portions of the Search Pattern	03/11/86

<u>U.S. Patent No</u>	<u>Entitled</u>	<u>Dated</u>
4,907,148	Cellular Array Processor with Individual Cell-Level Data-Dependent Cell Control and Multiport Input Memory	03-06-1990
4,983,958	Vector Selectable Coordinate-Addressable Dram Array	01-08-1991
5,122,984	Parallel Associative Memory System	06-16-1992
5,150,430	Lossless Data Compression Circuit and Method	09-22-1992
5,319,762	Associative Memory Capable of Matching a Variable Indicator in One String of Characters with Another String	06-07-1994
5,329,405	Associative Cam Apparatus and Method for Variable Length String Matching	07-12-1994
5,373,290	Apparatus and Method for Managing Multiple Dictionaries in Content Addressable Memory Based Data Compression	12-13-1994
5,440,753	Variable Length String Matcher	08-08-1995
5,448,733	Data Search and Compression Device and Method for Searching and Compressing Repeating Data	09-05-1995
5,497,488	System for Parallel String Search with a Function-Directed Parallel Collation of a First Partition of Each String Followed by Matching of Second Partitions	03-05-1996
5,602,764	Comparing Prioritizing Memory for String Searching in a Data Compression System	02-11-1997
5,640,582	Register Stacking in a Computer System	06-17-1997
5,818,873	Single Clock Cycle Data Compressor/Decompressor with a String Reversal Mechanism	10-06-1998
5,828,593	Large-Capacity Content Addressable Memory	10-27-1998
6,089,453	Article-Information Display System Using Electronically Controlled Tags	07-18-2000
6,295,534	Apparatus for Maintaining an Ordered List	09-25-2001
6,317,819	Digital Signal Processor Containing Scalar Processing of Vector Processors Operating from a Single Instruction	11-13-2001
6,542,989	Single Instruction Having OP Code and Stack Control Field	04-01-2003

#### OTHER DOCUMENTS

MITU et al., "A CMOS Implementation of a Connex Memory," pp 579-582, IEEE, 1997.

#### OTHER DOCUMENTS

STEFAN, Gheorghe, "Silicon or Molecules? What's the Best for Splicing?", Technical Univ. of Bucharest, Dept. of Electronics, pp 158-181, 1998.

STEFAN, Gheorghe, The Connex Memory. "A Physical Support for Tree/List Processing", Technical Univ. of Bucharest, Dept. of Electronics, pp 1-22, 1994.

STEFAN, Gheorge and BENEÄ, Robert, "Connex Memories & Rewriting Systems", Politehnica Univ. of Bucharest, Dept. of Electronics, pp 1299-1303, 1998.

STEFAN, Denisa and STEFAN, Gheorghe, "Bi-thread Microcontroller as Digital Signal Processor", 1997 IEEE

HASCSI, Zoltan, MITU, Bogdan, PETRE, Mariana and STEFAN, Gheorghe, "High-Level Synthesis of an Enhanced Connex Memory" 1996 IEEE, pages 163-166

STEFAN, G. and DRAGNICI, F., "Memory Management Unit With a Performant LRU Circuit" Polytechnic Institute of Bucharesi, pages 89-96, Jan 1991.

THIEBAUT, Dominique and STEFAN, Gheorghe, "Local Alignments of DNA Sequences with the Connex Engine" pp 1-12, Sept. 2001.

STEFAN, Gheorghe and MALITA, Mihaela, "The Splicing Mechanism and the Connex Memory" Technical Univ. of Bucharest, Dept. of Electronics; Univ. of Bucharest, Fac. of Mathematics, pgs. 225-229, 1997.

HASCSI, Zoltan and STEFAN, Gheorghe, "The Connex Content Addressable Memory (C2AM)" "Politehnica" University of Bucharest, Electronics & Telecommunications Dpt., pgs. 422-425, Sept 1995.


This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such a publication is prior art. Moreover, the Applicant understands that the Examiner is to make an independent evaluation of the cited information.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached form PTO/SB/08A and PTO/SB/08B with initials or other appropriate marks.

No fees are considered to be due; however, if it is determined that payment of a fee is required, please charge our deposit account No. 13-0235.

Respectfully submitted,

By

  
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PTO/SB/08a (06-03)

Approved for use through 07/31/2003. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

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of

3

**Complete if Known**

Application Number 10/728,234

Filing Date December 4, 2003

First Named Inventor Dan TOMESCU

Art Unit 2183

Examiner Name

Attorney Docket Number 5227-0001-1

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 4575818	03-11-1986	Almy, et al.	
		US- 4907148	03-06-1990	Morton	
		US- 4983958	01-08-1991	Carrick	
		US- 5122984	06-16-1992	Strehler	
		US- 5150430	09-22-1992	Chu	
		US- 5319762	06-07-1994	Mayer	
		US- 5329405	07-12-1994	Hou, et al.	
		US- 5373290	12-13-1994	Lempel, et al.	
		US- 5440753	08-08-1995	Hou, et al.	
		US- 5488733	09-05-1995	Satoh, et al.	
		US- 5497488	03-05-1996	Akizawa, et al.	
		US- 5602764	02-11-1997	Eskandari-Gharnin	
		US- 5640582	06-17-1997	Hays, et al.	
		US- 5818873	10-06-1998	Wall, et al.	
		US- 5828593	10-27-1998	Schultz, et al.	
		US- 6089453	07-18-2000	Kayser, et al.	
		US- 6295534	09-25-2001	Mann	
		US- 6317819	11-13-2001	Morton	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

Examiner  
SignatureDate  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

Sheet

2

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Application Number

**Complete if Known**

10/728.234

Filing Date

December 4, 2003

First Named Inventor

**Dan TOMESCU**

Art Unit

2183

Examiner Name

Attorney Docket Number

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		Country Code <sup>3</sup> *Number <sup>4</sup> *Kind Code <sup>5</sup> (if known)				

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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Application Number	10/728,234
Filing Date	December 4, 2003
First Named Inventor	Dan TOMESCU
Art Unit	2183
Examiner Name	
Attorney Docket Number	5227-0001-1

Sheet 3 of 3

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		MITU et al., "A CMOS Implementation of a Connex Memory," pp 579-582, IEEE, 1997.	
		STEFAN, Gheorghe, "Silicon or Molecules? What's the Best for Splicing?", Technical Univ. of Bucharest, Dept. of Electronics, pp 158-181, 1998.	
		STEFAN, Gheorghe, The Connex Memory. "A Physical Support for Tree/List Processing", Technical Univ. of Bucharest, Dept. of Electronics, pp 1-22, 1994.	
		STEFAN, Gheorge and BENEIA, Robert, "Connex Memories & Rewriting Systems", Politehnica Univ. of Bucharest, Dept. of Electronics, pp 1299-1303, 1998.	
		STEFAN, Denisa and STEFAN, Gheorghe, "Bi-thread Microcontroller as Digital Signal Processor", 1997 IEEE.	
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